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Date: June 9, 1999

Attorney Docket No. H-782

To: Assistant Commissioner for Patents
Washington, D.C. 20231

Sir: Transmitted herewith for filing is the patent application of:

Inventor: SEE ATTACHED LIST (H. ITO et al)

For:
LOGIC EMULATION MODULE AND
LOGIC EMULATION BOARD

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Bar Membership in Other Than Virginia
Bar Membership in District of Columbia



11 Sheets of Drawings



This application is being filed without an executed Declaration.



Priority is claimed from Japanese Application No. 10-161813
filed June 10, 1998. ☒ A certified copy is attached herewith.



Copies of the disclosure documents listed on the attached PTO 1449 form and
☒ discussed in the specification or ☒ attached Information Disclosure Statement.



A verified statement to establish small entity status under 37 CFR 1.9 and 1.27.



Specification: Abstract X, Description 28 pages; and 11 claim(s).



Preliminary Amendment.



Executed Declaration.

The filing fee is calculated as shown below:

Small Entity

Large Entity

For:	No. Filed	No. Extra
Basic Fee		
Total Claims	23 - 20 = *	3
Indep Claims	10 - 3 = *	7
<input type="checkbox"/> Multiple Dependent Claim (s)		

* If difference is less than zero
then enter '0' in second column

Rate	Fee
	\$ 380
x 9	\$
x 39	\$
+ 130	\$
Total	\$

OR

Rate	Fee
	\$ 760
x 18	\$ 54
x 78	\$ 546
+ 260	\$ 0
Total	\$ 1,360



A check in the amount of \$ 1,360.00 is enclosed for the filing fee.



The Commissioner is hereby authorized to charge any additional fees that may be required to
Deposit Account No. 02-1540. A duplicate of this sheet is attached.

Respectfully Submitted,

By:

John R. Mattingly

Registration No. 30,293

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